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Patent	Filing Date First Named Inventor		09/927,599					
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STATEMENT UNDER 37 CFR 3.73(b)						
Applicant/Patent Owner. Integrated Silicon Solution, Inc.						
Application No./Patent No.: 6,889,225 Filed/Issue Date: issued 05/03/2005						
Entitled: Segmented Bus Architecture For Improving Speed In Integrated Circuit Memories						
Integrated Silicon Solution, Inc. , a Corporation (Name of Assignee)						
(Type of Assignee, e.g., corporation, partnership states that it is:	, university, government agency, etc.)					
1. The assignee of the entire right, title, and interest; or						
an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)						
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Additional documents in the chain of title are listed on a supplemental sheet.						
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[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be a Division in accordance with 37 CFR Part 3, to record the assignment in the records of the 302.08]	submitted to Assignment ne USPTO. <u>See</u> MPEP					
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	87,4108					
Signature	Date					
Lou Yang	408-969-5124					
Printed or Typed Name	Telephone Number					
V.P. of Business Development Title	•]					

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